

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: R. William Ezell
Date Filed: July 28, 2003
Title: METHOD AND SYSTEM FOR FILTER TUNING USING A
DIGITAL TRIM VALUE

Mail Stop Patent Application
Commissioner of Patents
PO Box 1450
Alexandria, VA 22313-1450

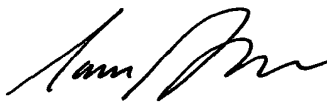
Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Applicant respectfully requests, pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified patent application. Copies of the references are enclosed for the convenience of the Examiner. No representation is made that a search has been made, that the references are material to the patentability of the present application, or that the references qualify as prior art.

Respectfully submitted,

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Date: July 28, 2003

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PTO-1449	Application No.	Applicant(s)	
	Docket Number 073671.0171	Group Art Unit	Filing Date July 28, 2003
Information Disclosure Citation In an Application		R. William Ezell	

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						
	K						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	L							
	M							
	N							
	O							

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
	P	Laker, et al., "Design of Analog Integrated Circuits and Systems," pp. 786-791 plus copyright page	1994
	Q		
	R		
	S		
	T		
	U		

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

U.S. PATENT AND TRADEMARK OFFICE

DAL01:748620.1